

5SNG 0150Q170300

62Pak phase leg IGBT Module

$V_{CE} = 1700\text{ V}$
 $I_C = 150\text{ A}$

Ultra low-loss, rugged SPT++ chip-set
 Smooth switching SPT++ chip-set for good EMC
 Cu base-plate for low thermal resistance
 Industry standard package
 2 switches in one package



Maximum rated values ¹⁾

Parameter	Symbol	Conditions	min	max	Unit
Collector-emitter voltage	V_{CES}	$V_{GE} = 0\text{ V}$, $T_{vj} \geq 25\text{ °C}$		1700	V
DC collector current	I_C	$T_C = 100\text{ °C}$, $T_{vj} = 175\text{ °C}$		150	A
Peak collector current	I_{CM}	$t_p = 1\text{ ms}$		300	A
Gate-emitter voltage	V_{GES}		-20	20	V
Total power dissipation	P_{tot}	$T_C = 25\text{ °C}$, $T_{vj} = 175\text{ °C}$, per switch		950	W
DC forward current	I_F			150	A
Peak forward current	I_{FRM}	$t_p = 1\text{ ms}$		300	A
Surge current	I_{FSM}	$V_R = 0\text{ V}$, $T_{vj} = 175\text{ °C}$, $t_p = 10\text{ ms}$, half-sinewave		750	A
IGBT short circuit SOA	t_{psc}	$V_{CC} = 1300\text{ V}$, $V_{CEM\ CHIP} \leq 1700\text{ V}$ $V_{GE} \leq 15\text{ V}$, $T_{vj} \leq 175\text{ °C}$		10	μs
Isolation voltage	V_{isol}	1 min, $f = 50\text{ Hz}$		4000	V
Junction temperature	T_{vj}			175	$^{\circ}\text{C}$
Junction operating temperature	$T_{vj(op)}$		-40	175	$^{\circ}\text{C}$
Case temperature	T_C		-40	125 ²⁾ /150	$^{\circ}\text{C}$
Storage temperature	T_{stg}		-40	125	$^{\circ}\text{C}$
Mounting torques ³⁾	M_s	Base-heatsink, M6 screws	3	6	Nm
	M_{t1}	Main terminals, M6 screws	3	6	

¹⁾ Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747

²⁾ for UL1557 compliance T_{Cmax} must be limited to 125 $^{\circ}\text{C}$

³⁾ for detailed mounting instructions refer to ABB Document No. 5SYA 2106

IGBT characteristic values ⁴⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector (-emitter) breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0 \text{ V}$, $I_C = 10 \text{ mA}$, $T_{vj} = 25 \text{ }^\circ\text{C}$	1700			V
Collector-emitter ⁵⁾ saturation voltage	$V_{CE \text{ sat}}$	$I_C = 150 \text{ A}$, $V_{GE} = 15 \text{ V}$	$T_{vj} = 25 \text{ }^\circ\text{C}$	2.25	2.6	V
			$T_{vj} = 125 \text{ }^\circ\text{C}$	2.55		V
			$T_{vj} = 175 \text{ }^\circ\text{C}$	2.75		V
Collector cut-off current	I_{CES}	$V_{CE} = 1700 \text{ V}$, $V_{GE} = 0 \text{ V}$	$T_{vj} = 25 \text{ }^\circ\text{C}$		0.5	mA
			$T_{vj} = 125 \text{ }^\circ\text{C}$	0.75		mA
			$T_{vj} = 175 \text{ }^\circ\text{C}$	15		mA
Gate leakage current	I_{GES}	$V_{CE} = 0 \text{ V}$, $V_{GE} = \pm 20 \text{ V}$, $T_{vj} = 175 \text{ }^\circ\text{C}$	-1		1	μA
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 6 \text{ mA}$, $V_{CE} = V_{GE}$, $T_{vj} = 25 \text{ }^\circ\text{C}$	4.5		6.5	V
Gate charge	Q_G	$I_C = 150 \text{ A}$, $V_{CE} = 900 \text{ V}$, $V_{GE} = -15 \text{ V} \dots 15 \text{ V}$		1.1		μC
Input capacitance	C_{ies}	$V_{CE} = 25 \text{ V}$, $V_{GE} = 0 \text{ V}$, $f = 1 \text{ MHz}$, $T_{vj} = 25 \text{ }^\circ\text{C}$		9.6		nF
Output capacitance	C_{oes}			0.85		nF
Reverse transfer capacitance	C_{res}			0.8		nF
Internal gate resistance	R_{Gint}	per switch		3.9		Ω
Turn-on delay time	$t_{d(on)}$	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	180		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	190		ns
			$T_{vj} = 175 \text{ }^\circ\text{C}$	190		ns
Rise time	t_r	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	66		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	68		ns
			$T_{vj} = 175 \text{ }^\circ\text{C}$	73		ns
Turn-off delay time	$t_{d(off)}$	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	220		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	245		ns
			$T_{vj} = 175 \text{ }^\circ\text{C}$	260		ns
Fall time	t_f	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	185		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	195		ns
			$T_{vj} = 175 \text{ }^\circ\text{C}$	205		ns
Turn-on switching energy	E_{on}	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	43		mJ
			$T_{vj} = 125 \text{ }^\circ\text{C}$	57		mJ
			$T_{vj} = 175 \text{ }^\circ\text{C}$	73		mJ
Turn-off switching energy	E_{off}	$V_{CC} = 900 \text{ V}$, $I_C = 150 \text{ A}$, $R_G = 2.7 \text{ }^\circ\Omega$, $V_{GE} = \pm 15 \text{ V}$, $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	22		mJ
			$T_{vj} = 125 \text{ }^\circ\text{C}$	37		mJ
			$T_{vj} = 175 \text{ }^\circ\text{C}$	46		mJ
Short circuit current	I_{SC}	$t_{psc} \leq 10 \text{ }^\circ\mu\text{s}$, $V_{GE} = 15 \text{ V}$, $V_{CC} = 1300 \text{ V}$, $V_{CEM \text{ CHIP}} \leq 1700 \text{ V}$	$T_{vj} = 175 \text{ }^\circ\text{C}$	480		A

⁴⁾ Characteristic values according to IEC 60747 - 9

⁵⁾ Collector-emitter saturation voltage is given at chip level

Diode characteristic values ⁶⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward voltage ⁷⁾	V_F	$I_F = 150 \text{ A}$	$T_{vj} = 25 \text{ }^\circ\text{C}$	1.6	2.2	V
			$T_{vj} = 125 \text{ }^\circ\text{C}$	1.75		V
			$T_{vj} = 175 \text{ }^\circ\text{C}$	1.7		V
Peak reverse recovery current	I_{RM}		$T_{vj} = 25 \text{ }^\circ\text{C}$	175		A
			$T_{vj} = 125 \text{ }^\circ\text{C}$	190		A
			$T_{vj} = 175 \text{ }^\circ\text{C}$	210		A
Recovered charge	Q_r	$V_{CC} = 900 \text{ V}$, $I_F = 150 \text{ A}$, $V_{GE} = \pm 15 \text{ V}$, $R_G = 2.7 \text{ } \Omega$, $di/dt = 3 \text{ kA}/\mu\text{s}$ $L_\sigma = 60 \text{ nH}$, inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	42		μC
			$T_{vj} = 125 \text{ }^\circ\text{C}$	70		μC
			$T_{vj} = 175 \text{ }^\circ\text{C}$	98		μC
Reverse recovery time	t_{rr}		$T_{vj} = 25 \text{ }^\circ\text{C}$	430		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	580		ns
			$T_{vj} = 175 \text{ }^\circ\text{C}$	840		ns
Reverse recovery energy	E_{rec}		$T_{vj} = 25 \text{ }^\circ\text{C}$	25		mJ
			$T_{vj} = 125 \text{ }^\circ\text{C}$	40		mJ
			$T_{vj} = 175 \text{ }^\circ\text{C}$	56		mJ

⁶⁾ Characteristic values according to IEC 60747 - 2

⁷⁾ Forward voltage is given at chip level

Package properties ⁸⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
IGBT thermal resistance junction to case	$R_{th(j-c)IGBT}$	per switch			0.135	K/W
Diode thermal resistance junction to case	$R_{th(j-c)DIODE}$				0.200	K/W
IGBT thermal resistance ³⁾ case to heatsink	$R_{th(c-s)IGBT}$	IGBT per switch, λ grease = $1\text{W}/\text{m} \times \text{K}$		0.033		K/W
Diode thermal resistance ³⁾ case to heatsink	$R_{th(c-s)DIODE}$	Diode per switch, λ grease = $1\text{W}/\text{m} \times \text{K}$		0.050		K/W
Comparative tracking index	CTI		200			
Module stray inductance	$L_{\sigma CE}$	per switch		20		nH
Resistance, terminal-chip	$R_{CC' \rightarrow EE'}$	per switch	$T_C = 25 \text{ }^\circ\text{C}$	0.7		m Ω
			$T_C = 125 \text{ }^\circ\text{C}$	0.98		
			$T_C = 175 \text{ }^\circ\text{C}$	1.12		

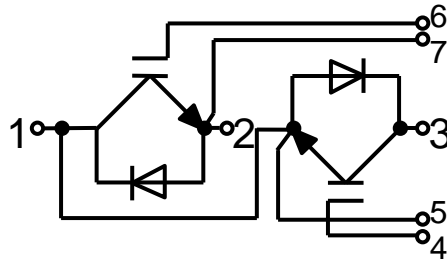
³⁾ For detailed mounting instructions refer to ABB Document No. 5SYA 2106

Mechanical properties ⁸⁾

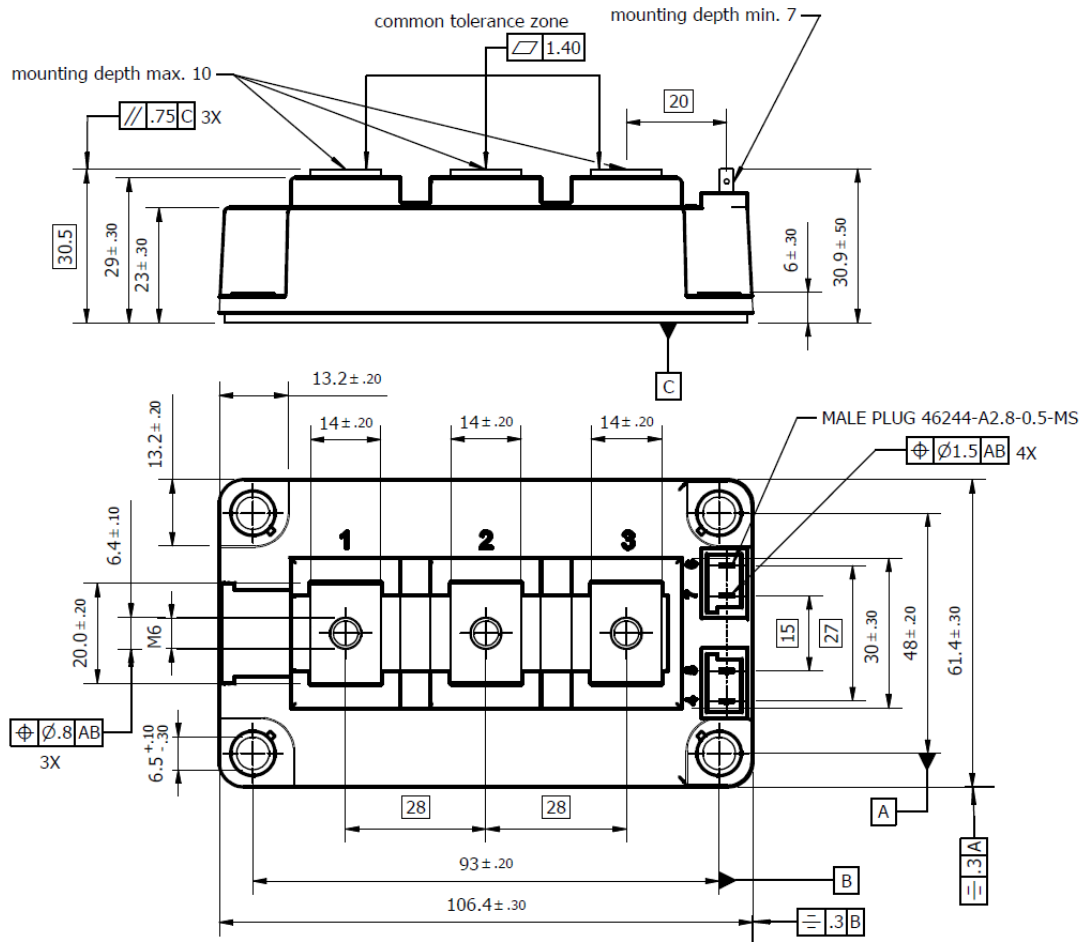
Parameter	Symbol	Conditions	min	typ	max	Unit
Dimensions	L x W x H	Typical	106.4 x 61.4 x 30.9			mm
Clearance distance in air	d_a	according to IEC 60664-1 and EN 50124-1	Term. to base:	23		mm
			Term. to term:	11		
Surface creepage distance	d_s	according to IEC 60664-1 and EN 50124-1	Term. to base:	29		mm
			Term. to term:	23		
Mass	m			330		g

⁸⁾ Package and mechanical properties according to IEC 60747 - 15

Electrical configuration



Outline drawing ³⁾



Note: all dimensions are shown in millimeters

³⁾ For detailed mounting instructions refer to ABB Document No. 5SYA 2106

This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. VIII.
This product has been designed and qualified for Industrial Level.

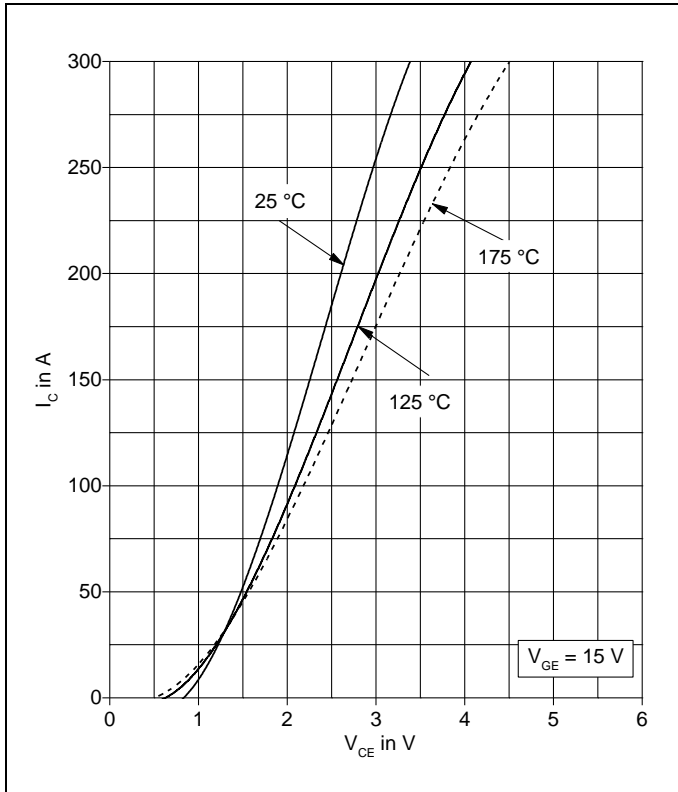


Fig. 1 Typical on-state characteristics, chip level

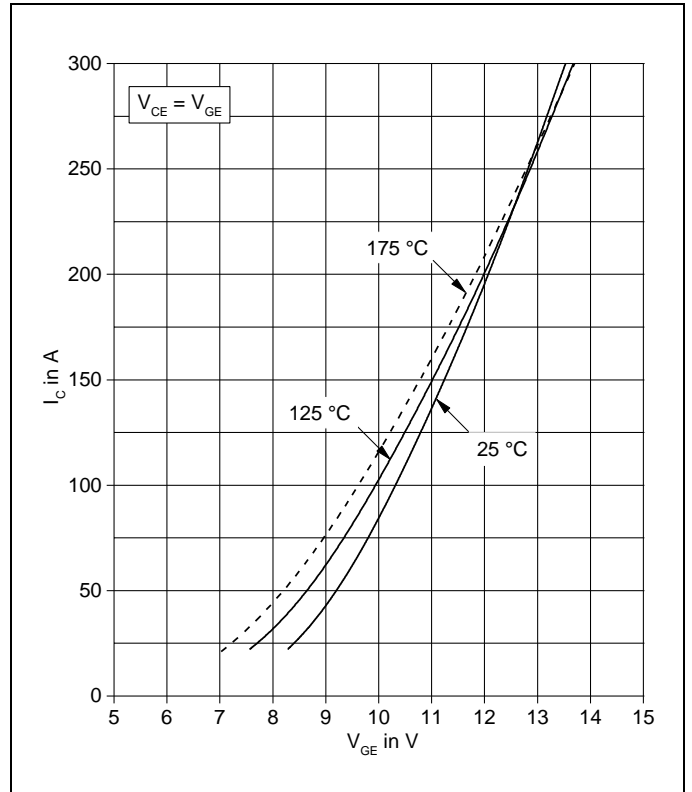


Fig. 2 Typical transfer characteristics, chip level

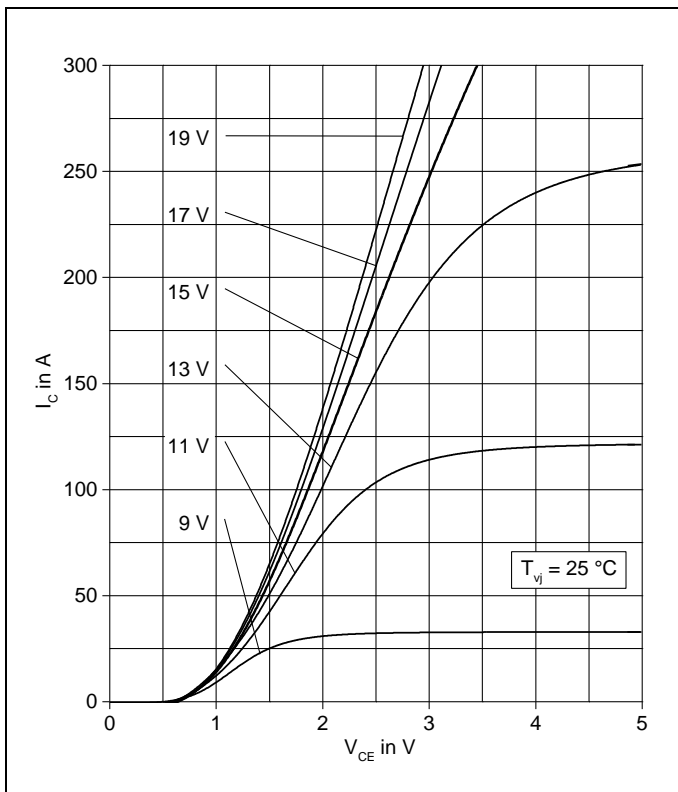


Fig. 3 Typical output characteristics, chip level

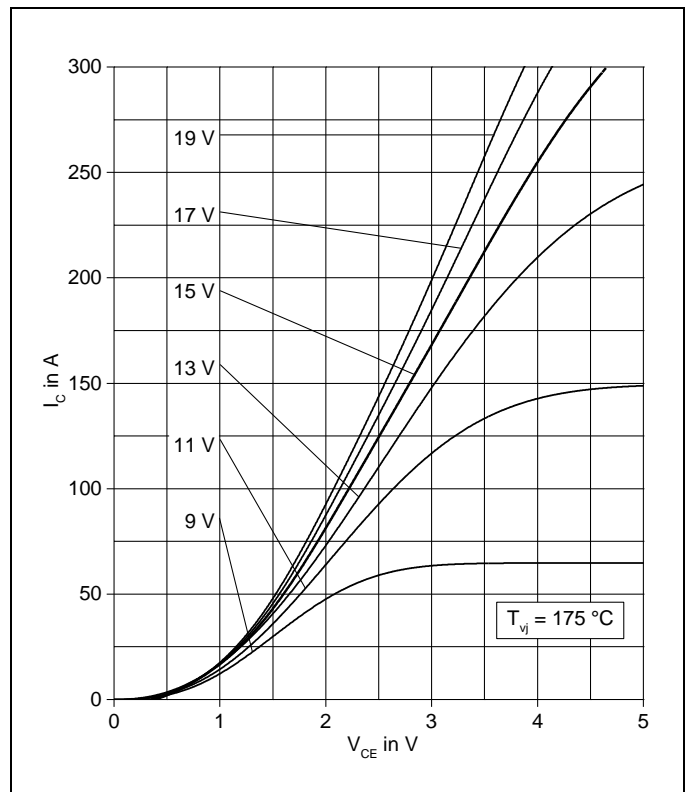


Fig. 4 Typical output characteristics, chip level

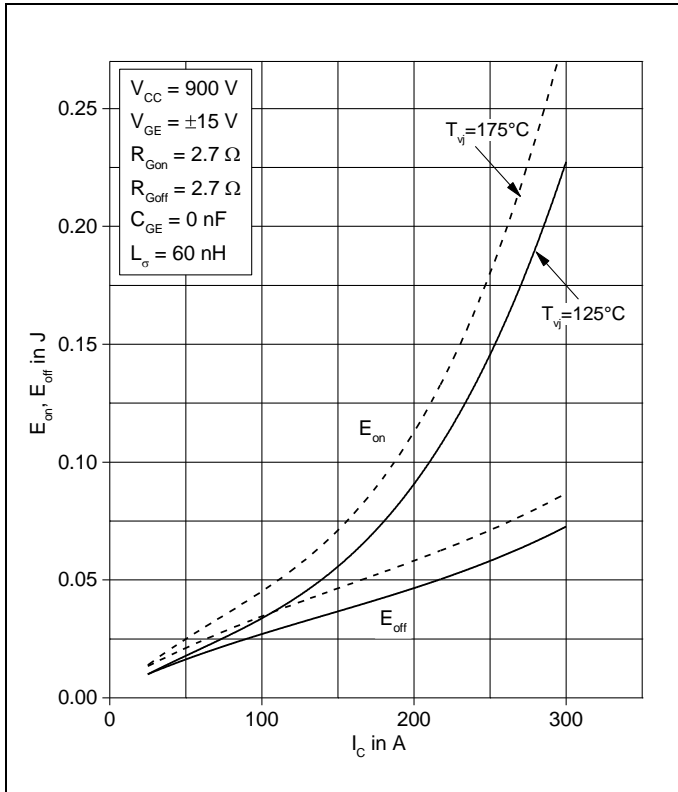


Fig. 5 Typical switching energies per pulse vs. collector current

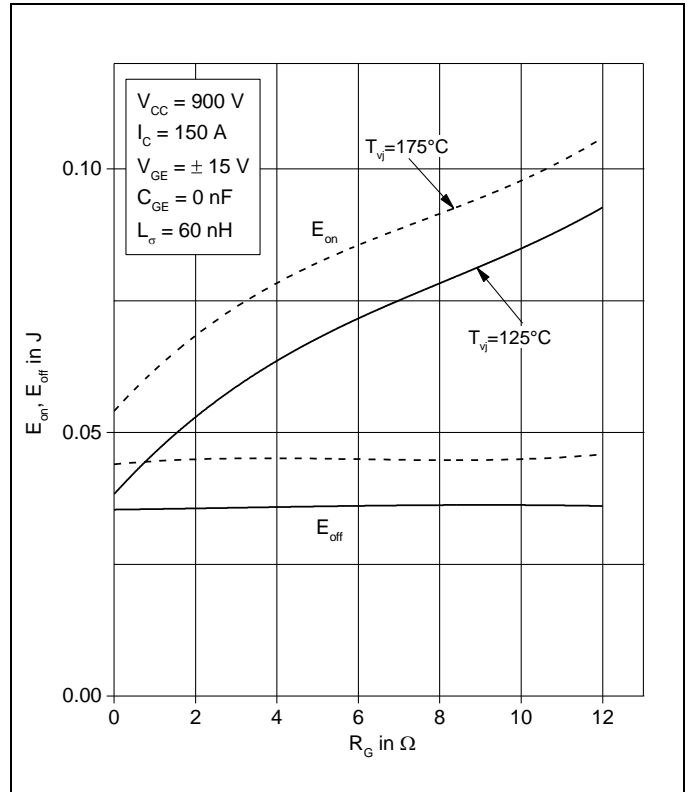


Fig. 6 Typical switching energies per pulse vs. gate resistor

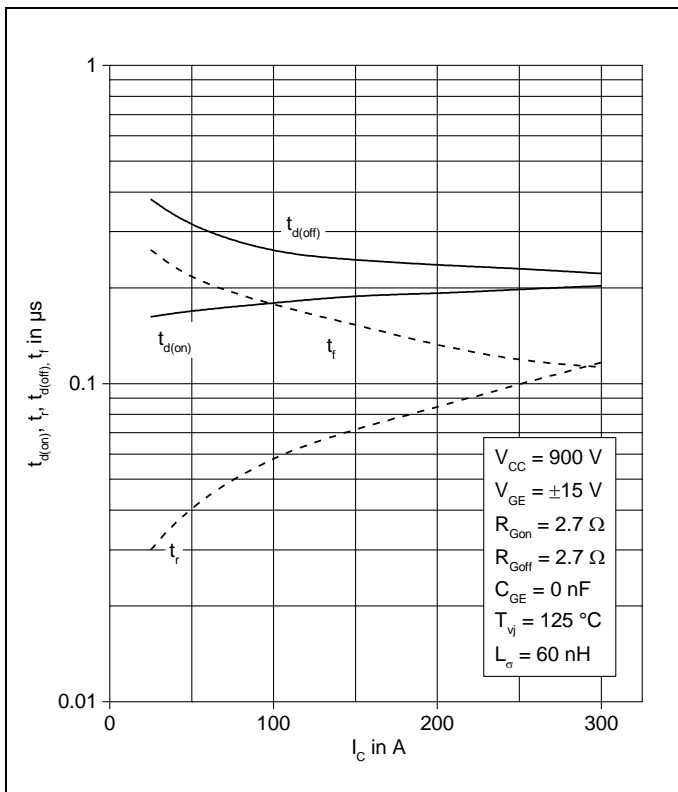


Fig. 7 Typical switching times vs. collector current

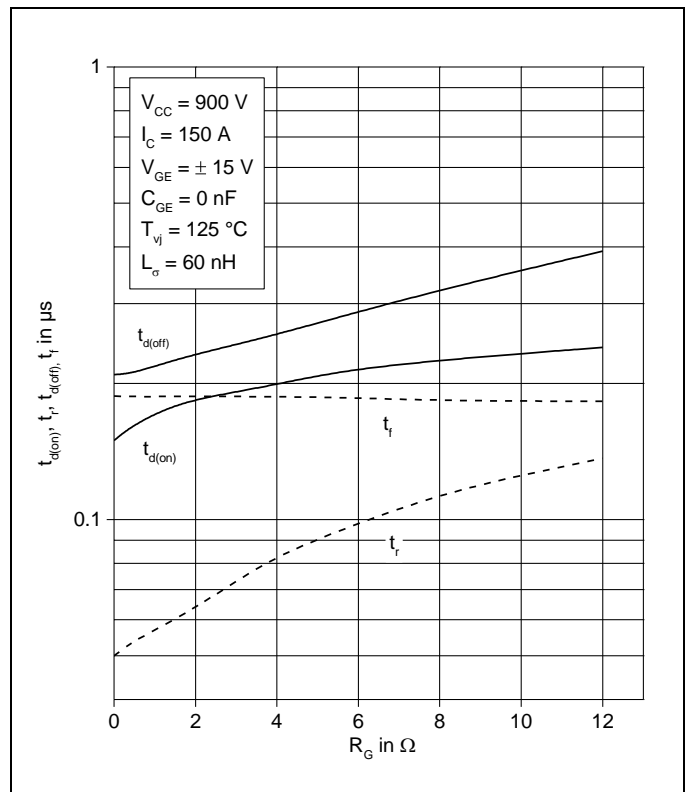


Fig. 8 Typical switching times vs. gate resistor

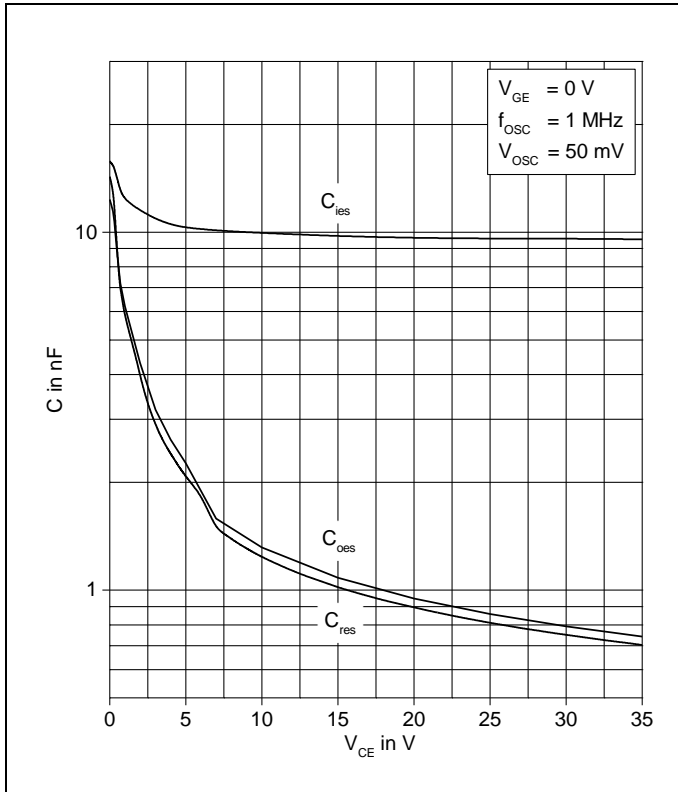


Fig. 9 Typical capacitances vs. collector-emitter voltage

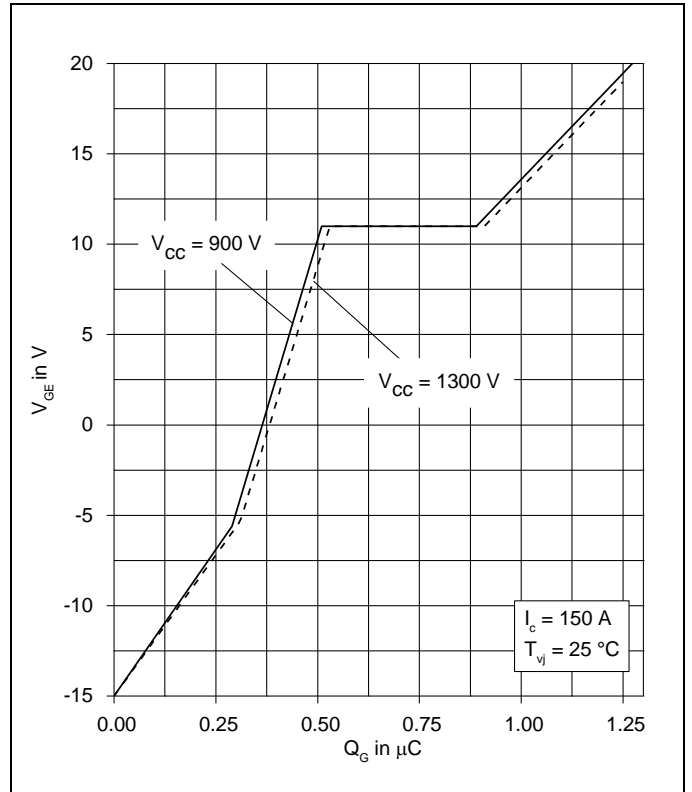


Fig. 10 Typical gate charge characteristics

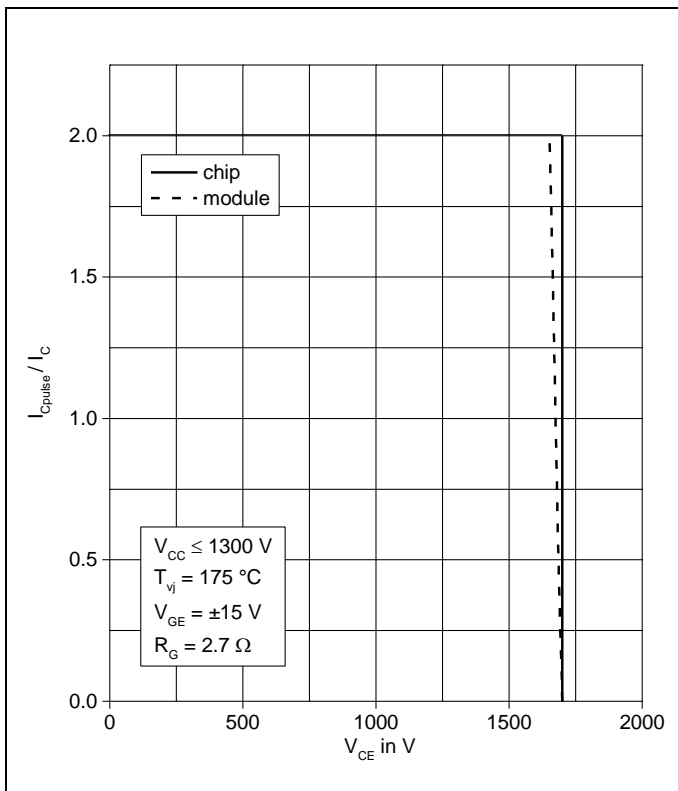


Fig. 11 Turn-off safe operating area (RBSOA)

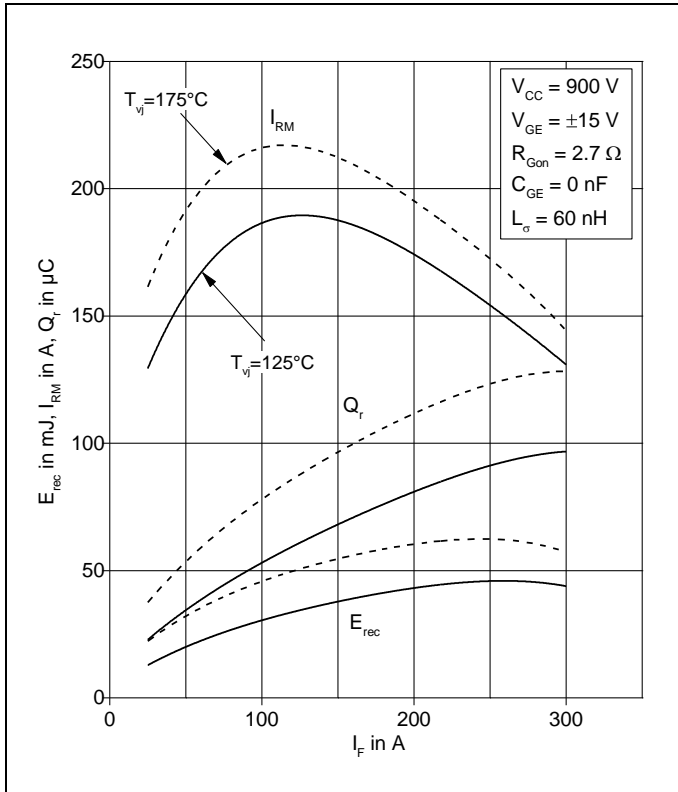


Fig. 12 Typical reverse recovery characteristics vs. forward current

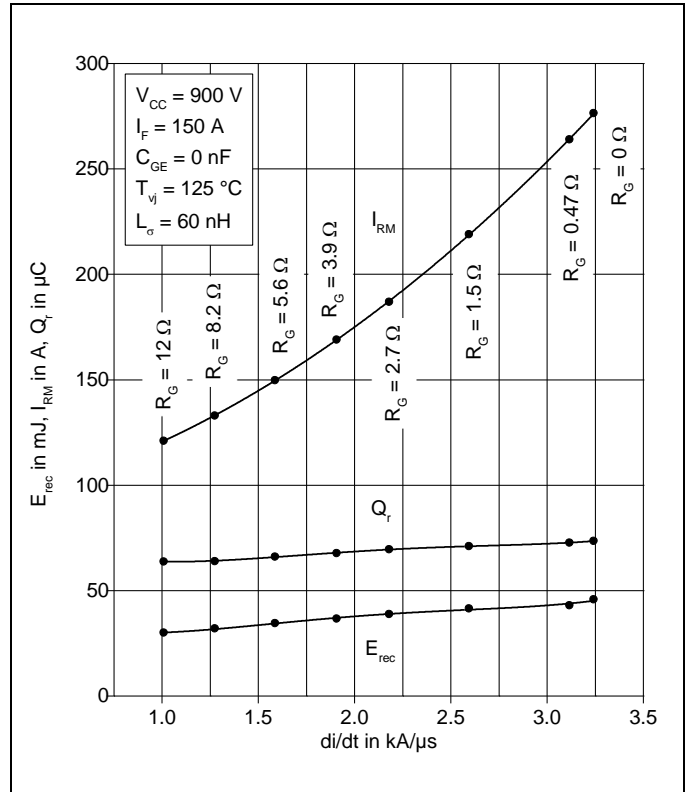


Fig. 13 Typical reverse recovery characteristics vs. di/dt

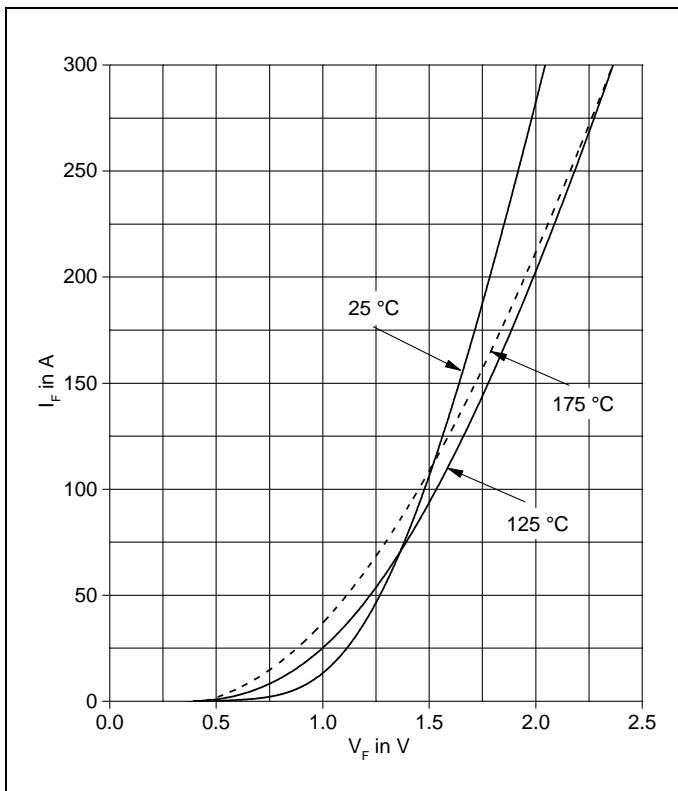


Fig. 14 Typical diode forward characteristics, chip level

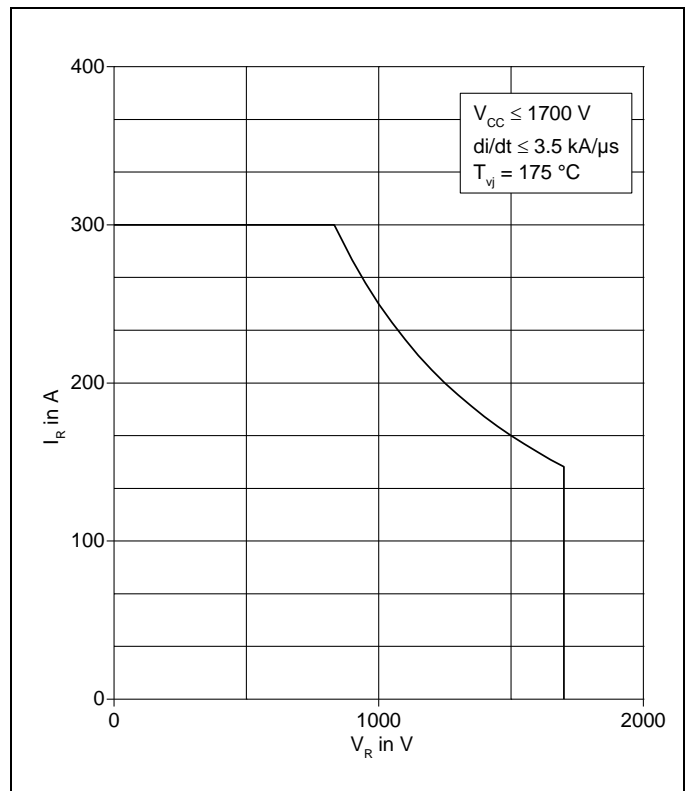


Fig. 15 Safe operating area diode (SOA)

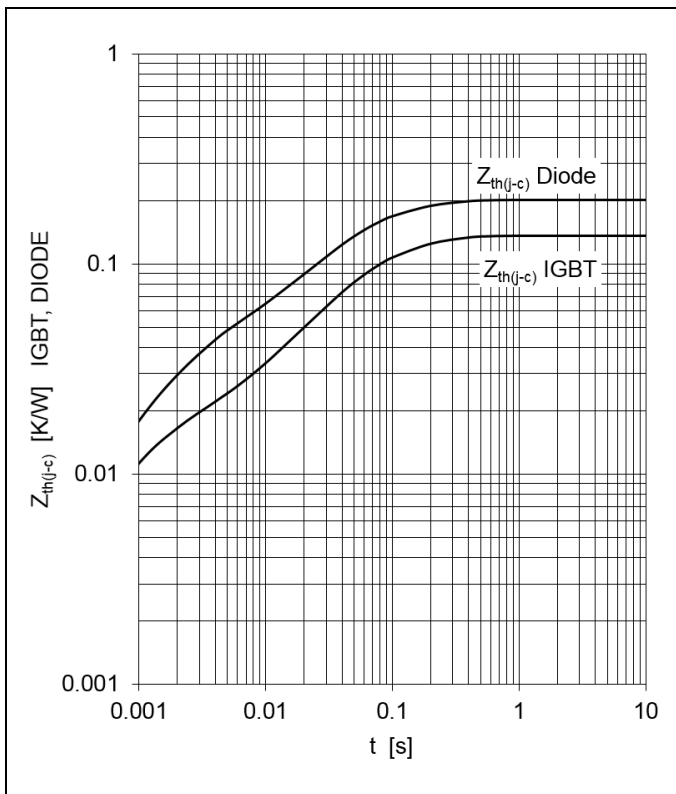


Fig. 16 Thermal impedance vs. time

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i (1 - e^{-t/\tau_i})$$

IGBT	R_i (K/kW)	13.7	74.6	46.7		
	τ_i (ms)	0.9	40.2	136		
DIODE	R_i (K/kW)	33.6	51.6	114.9		
	τ_i (ms)	1.8	137	37.2		

Related documents:

- 5SYA 2042 Failure rates of IGBT modules due to cosmic rays
- 5SYA 2045 Thermal runaway during blocking
- 5SYA 2053 Applying IGBT
- 5SYA 2058 Surge currents for IGBT diodes
- 5SYA 2093 Thermal design and temperature ratings of IGBT modules
- 5SYA 2098 Paralleling of IGBT modules

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